

FIG. 1

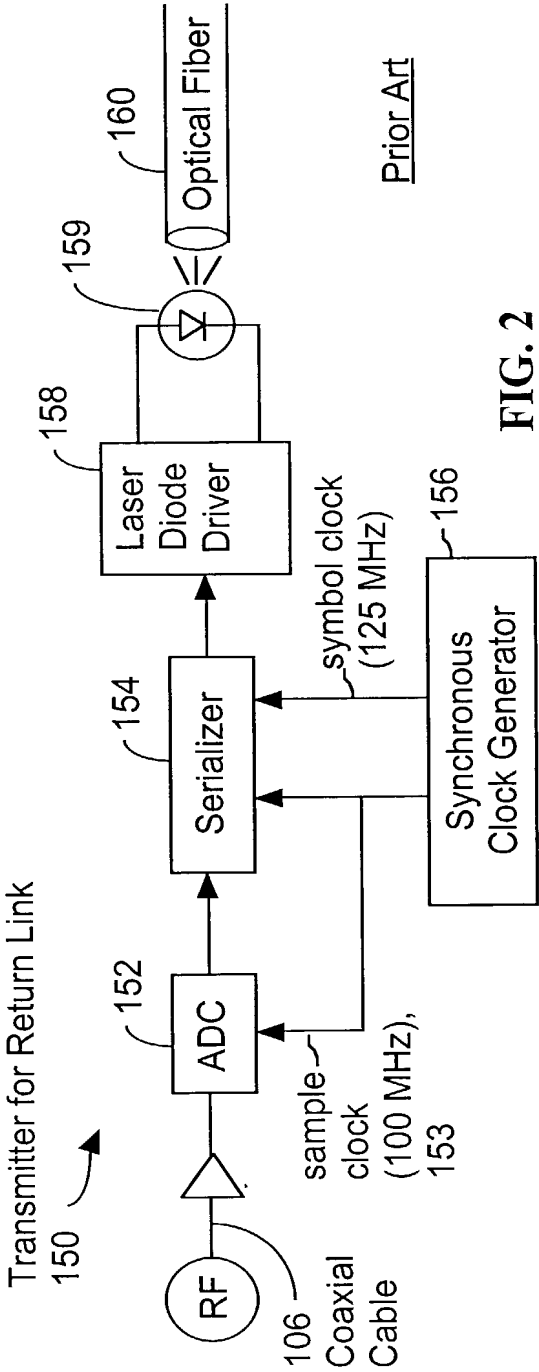


FIG. 2

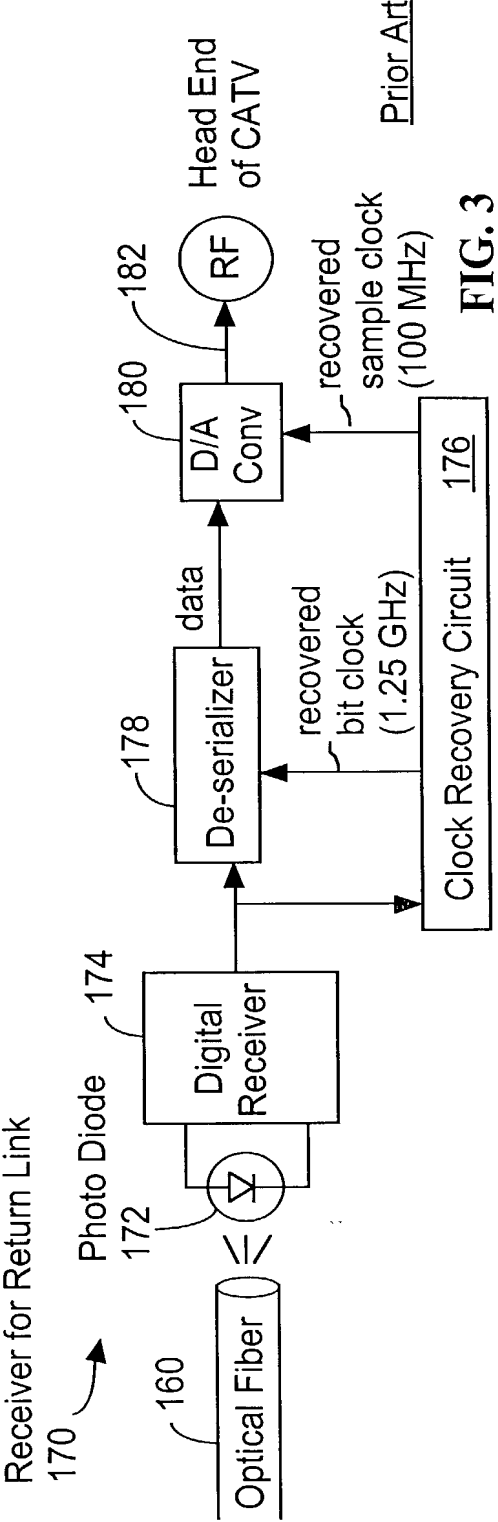


FIG. 3

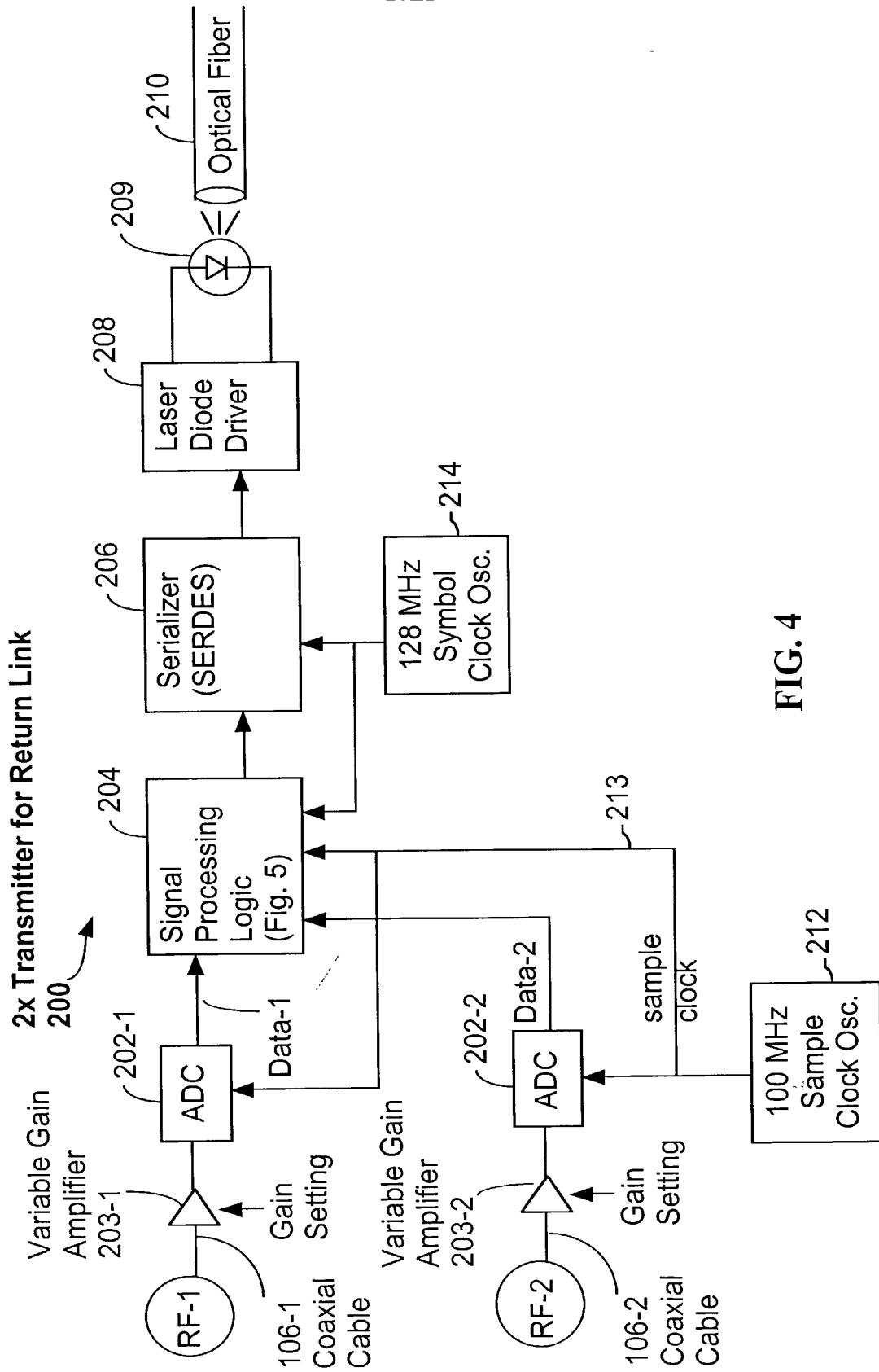
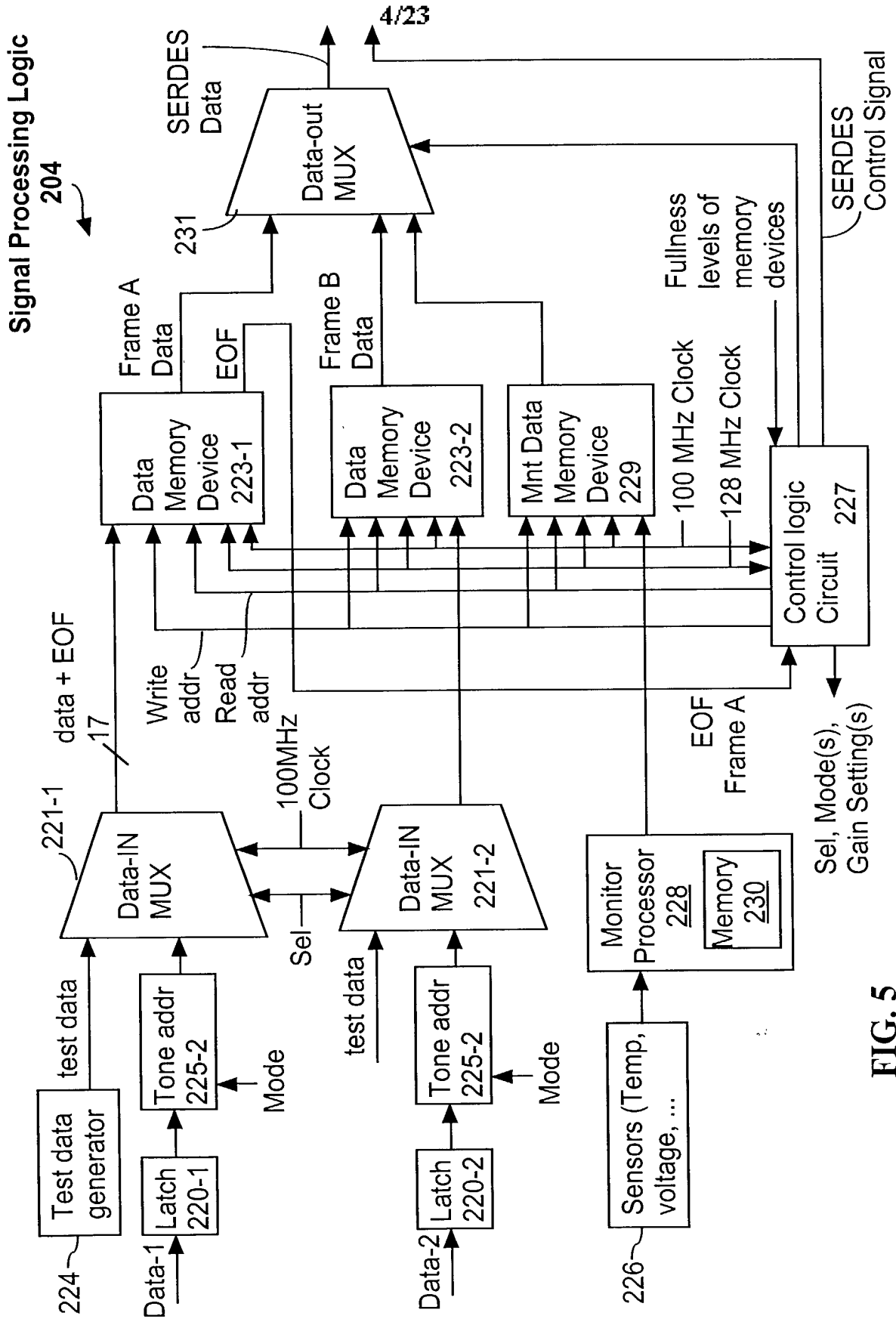


FIG. 4



**FIG. 5**

FIG. 6A

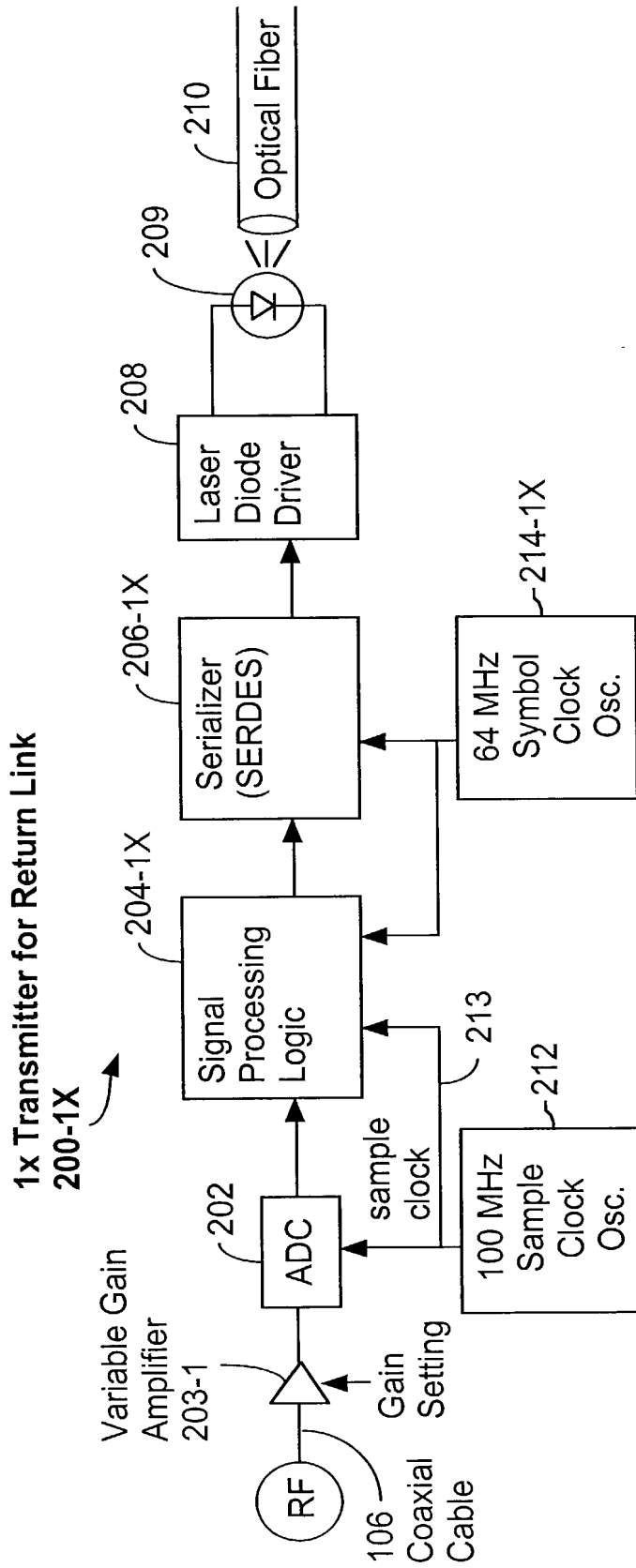
|   |   |   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|---|---|
| A | B | A | B | A | B | A | B | A | B | A |
|---|---|---|---|---|---|---|---|---|---|---|

FIG. 6B

[illegible]

FIG. 6C

|   |   |   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|---|---|
| A | B | I | C | M | M | A | B | I | C | M |
|---|---|---|---|---|---|---|---|---|---|---|



**FIG. 7**

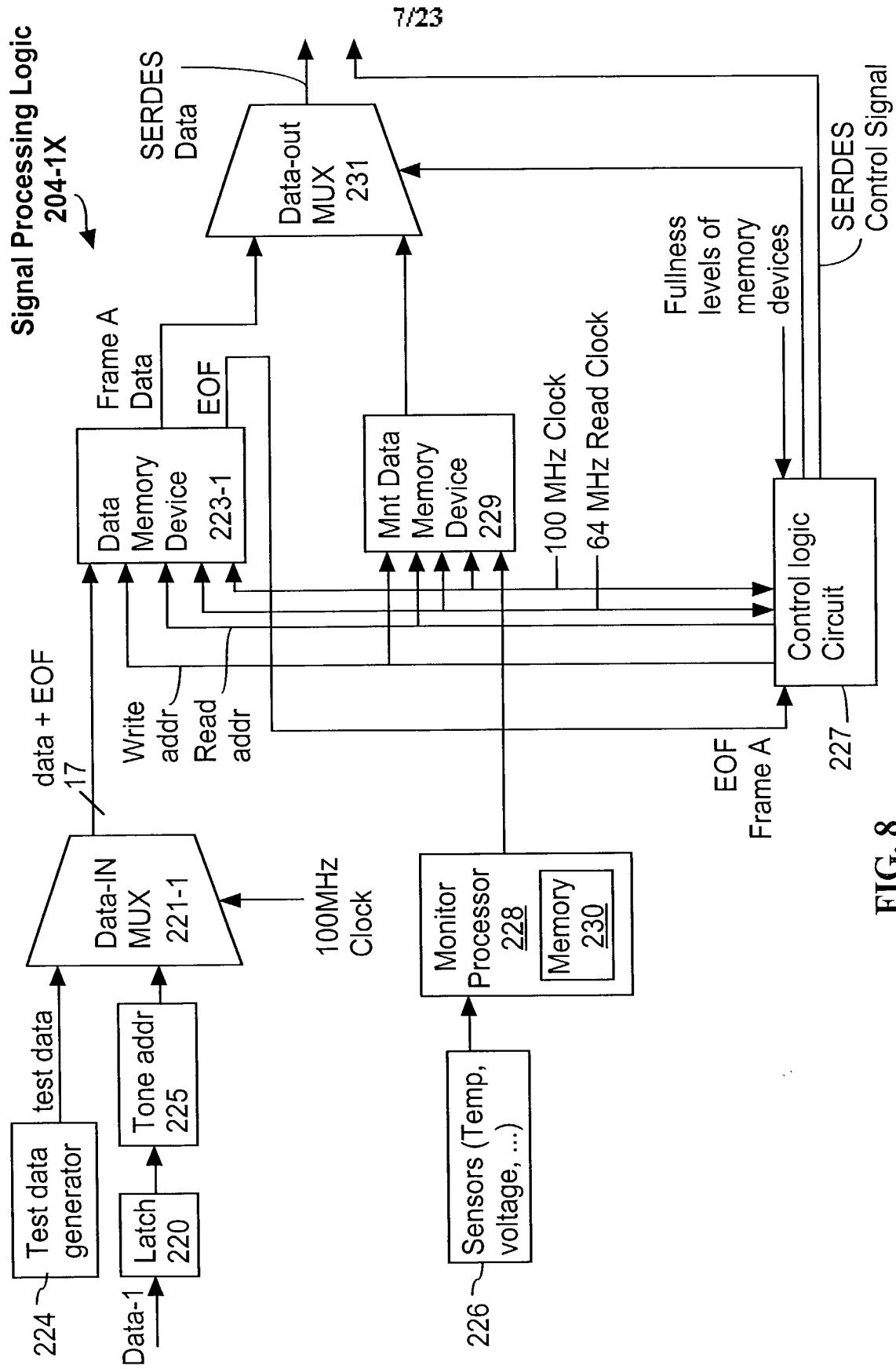
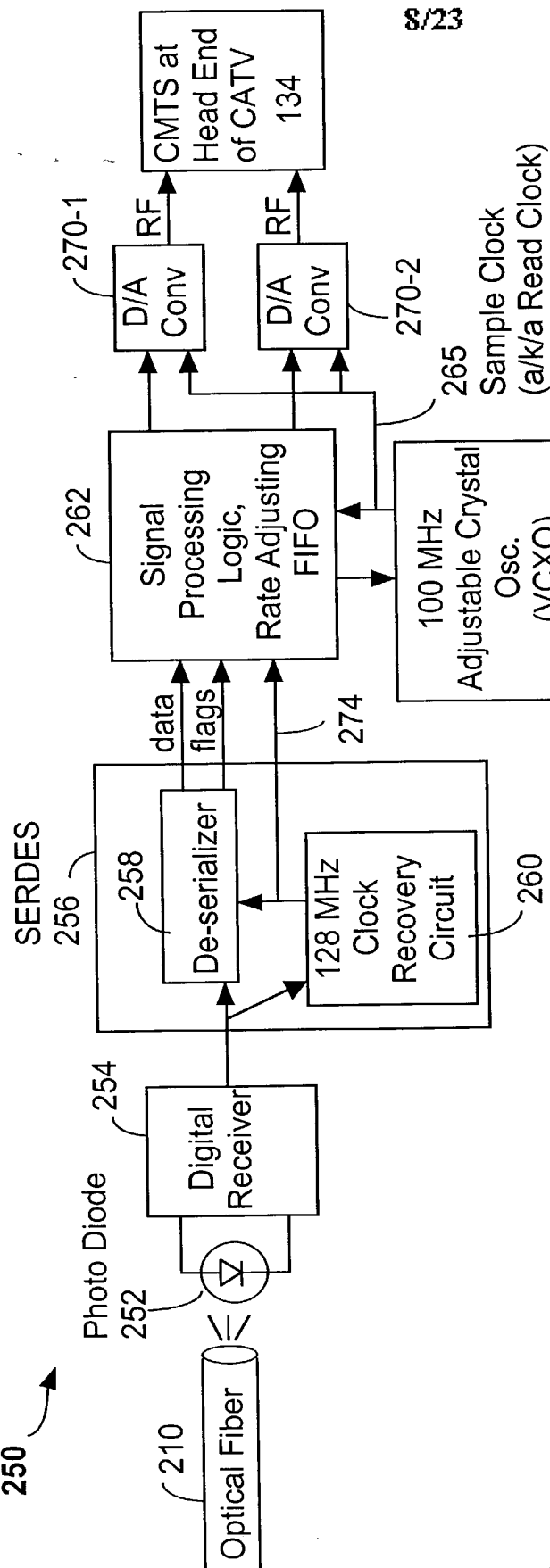


FIG. 8

# Receiver for Return Link



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FIG. 9

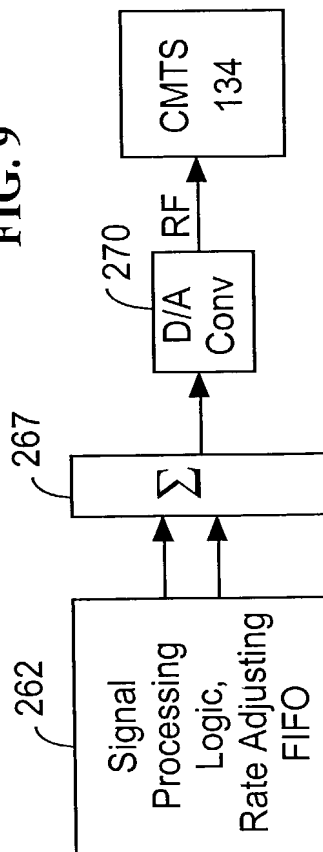


FIG. 9A



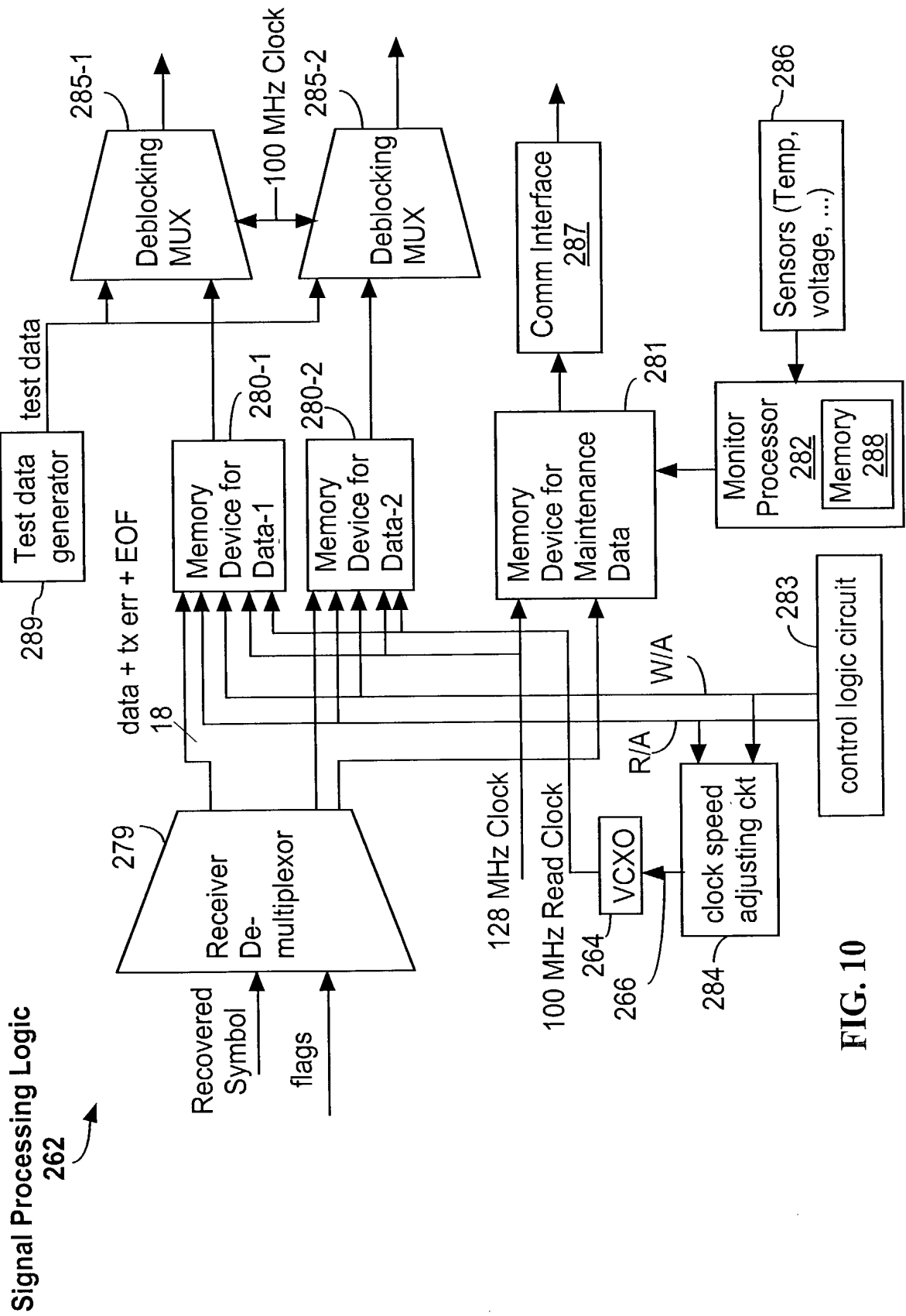
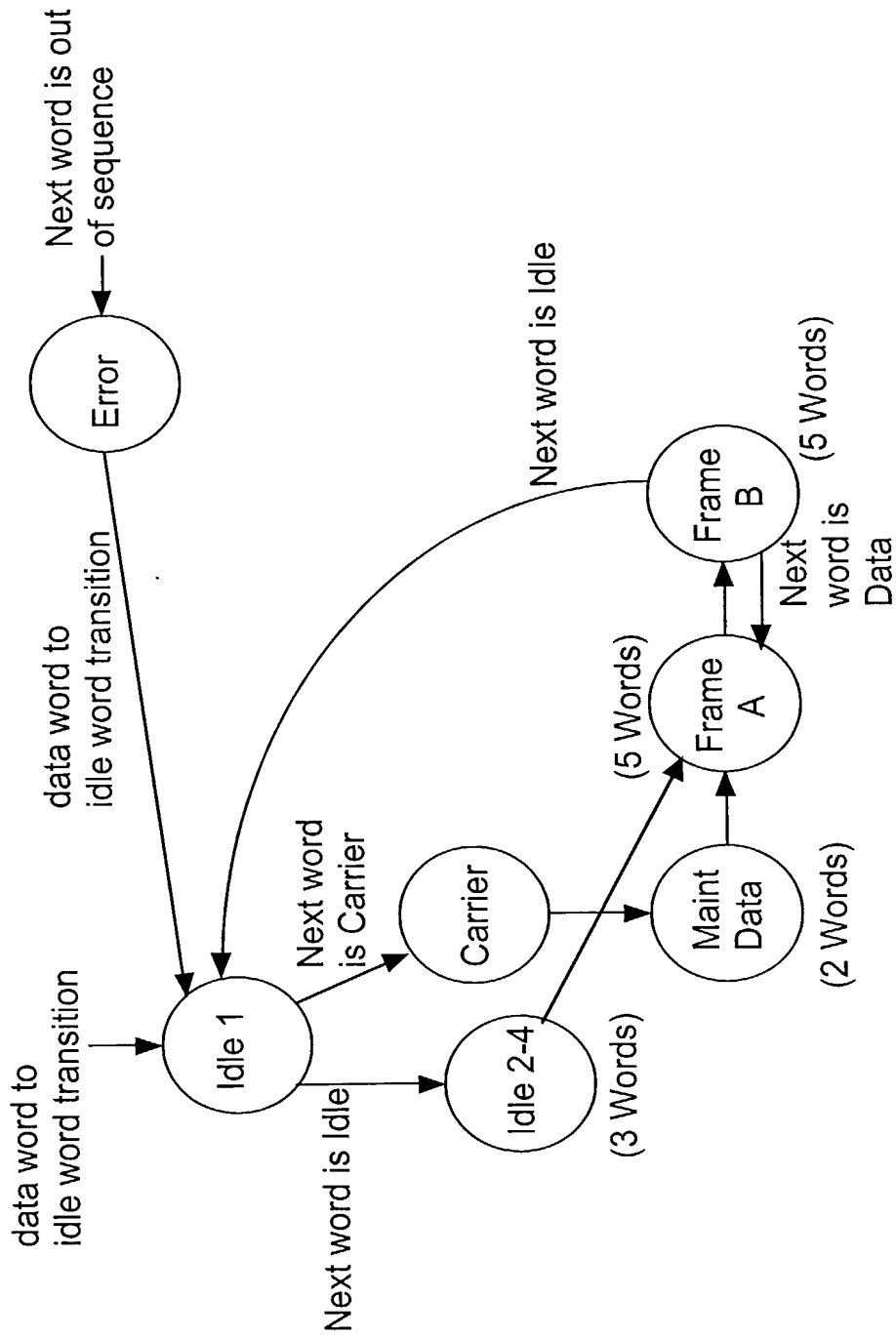


FIG. 10



State Diagram for Dual Channel Receiver Demultiplexor

**FIG. 11**

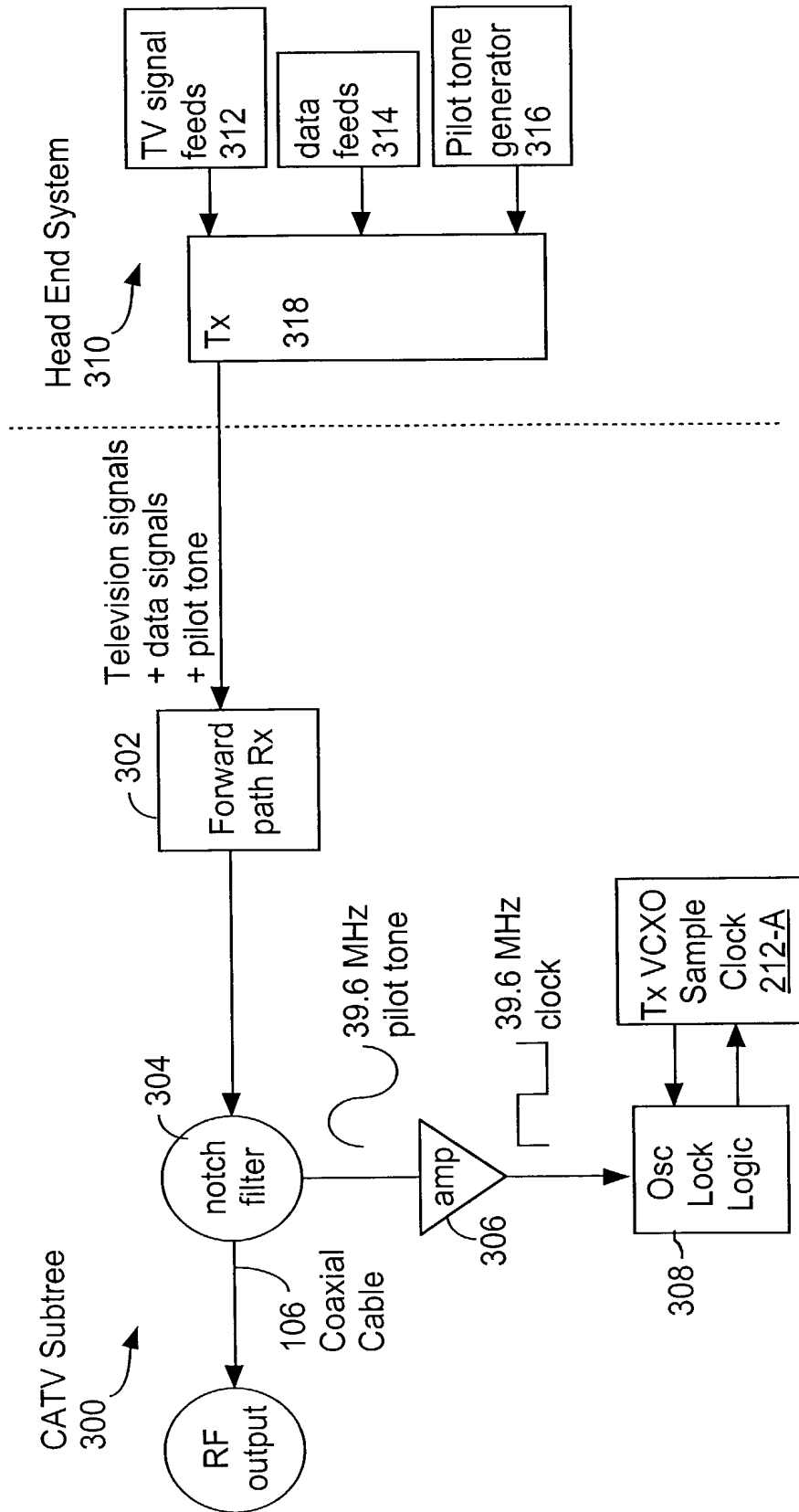


FIG. 12

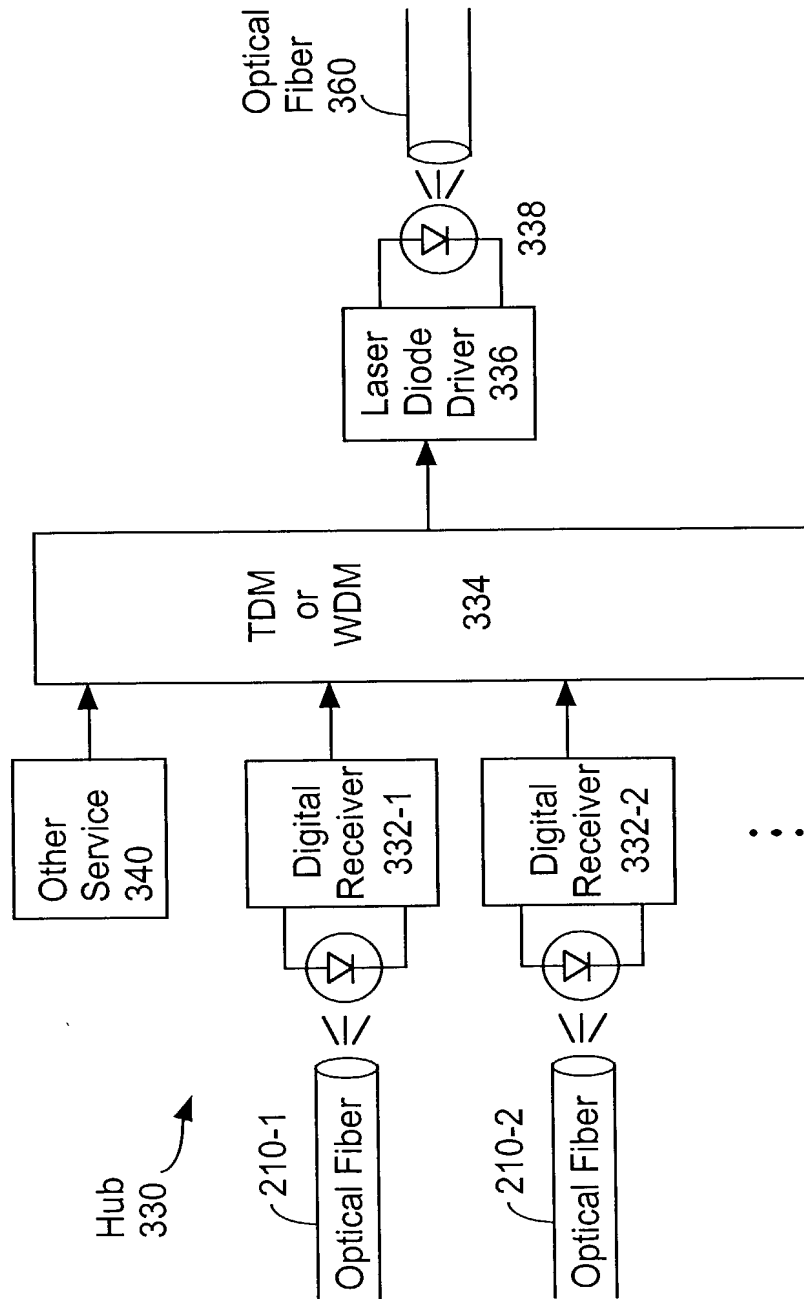


FIG. 13

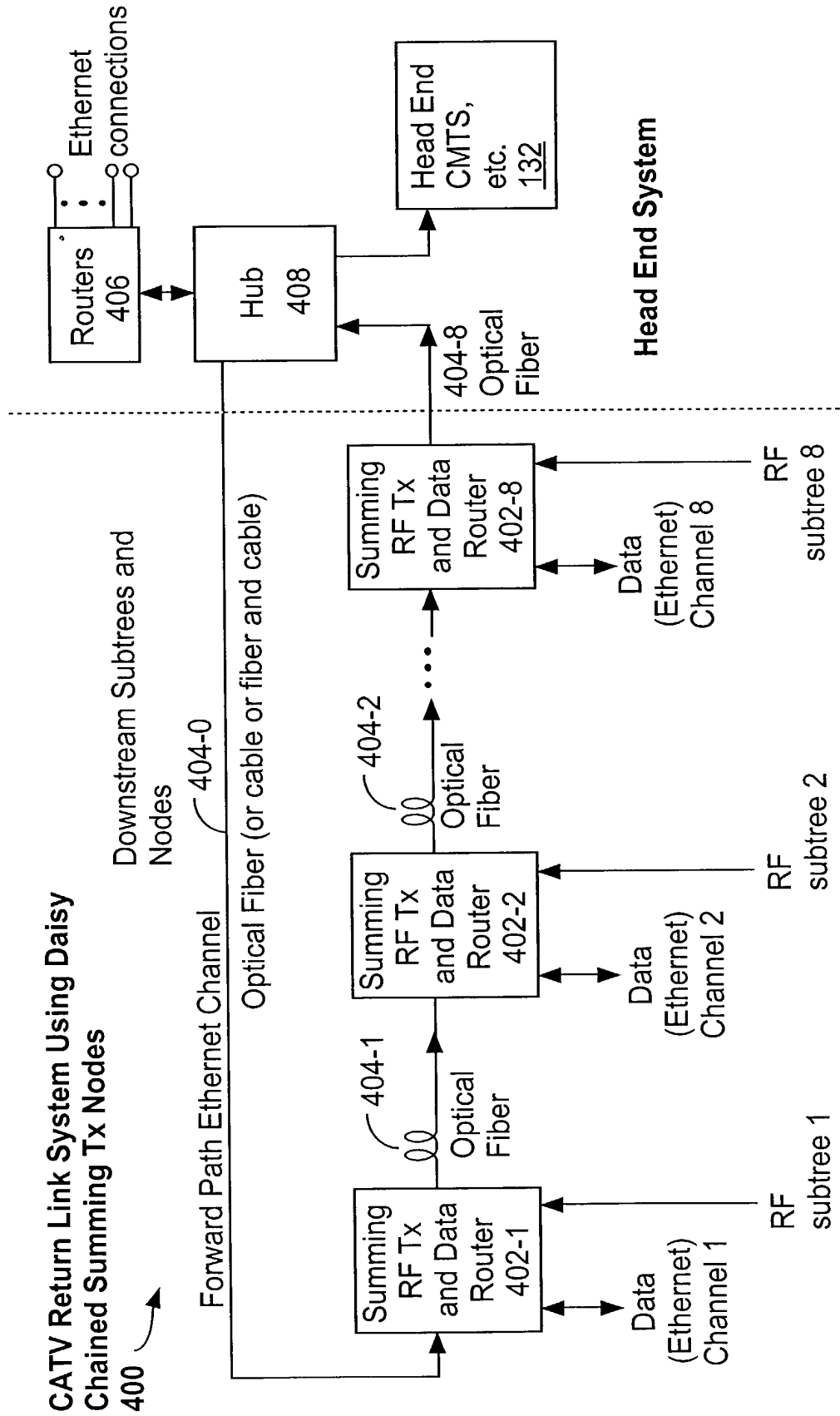


FIG. 14

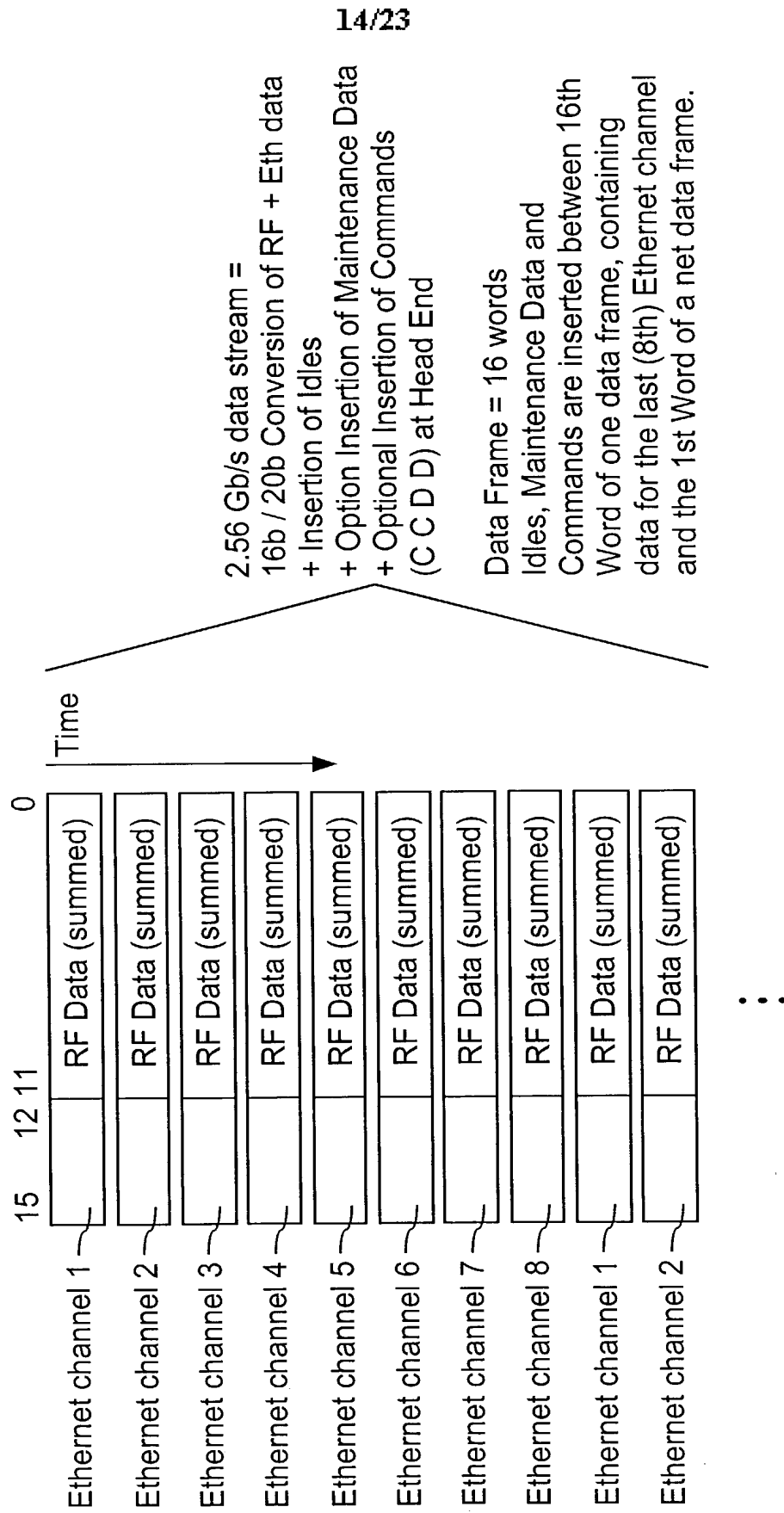
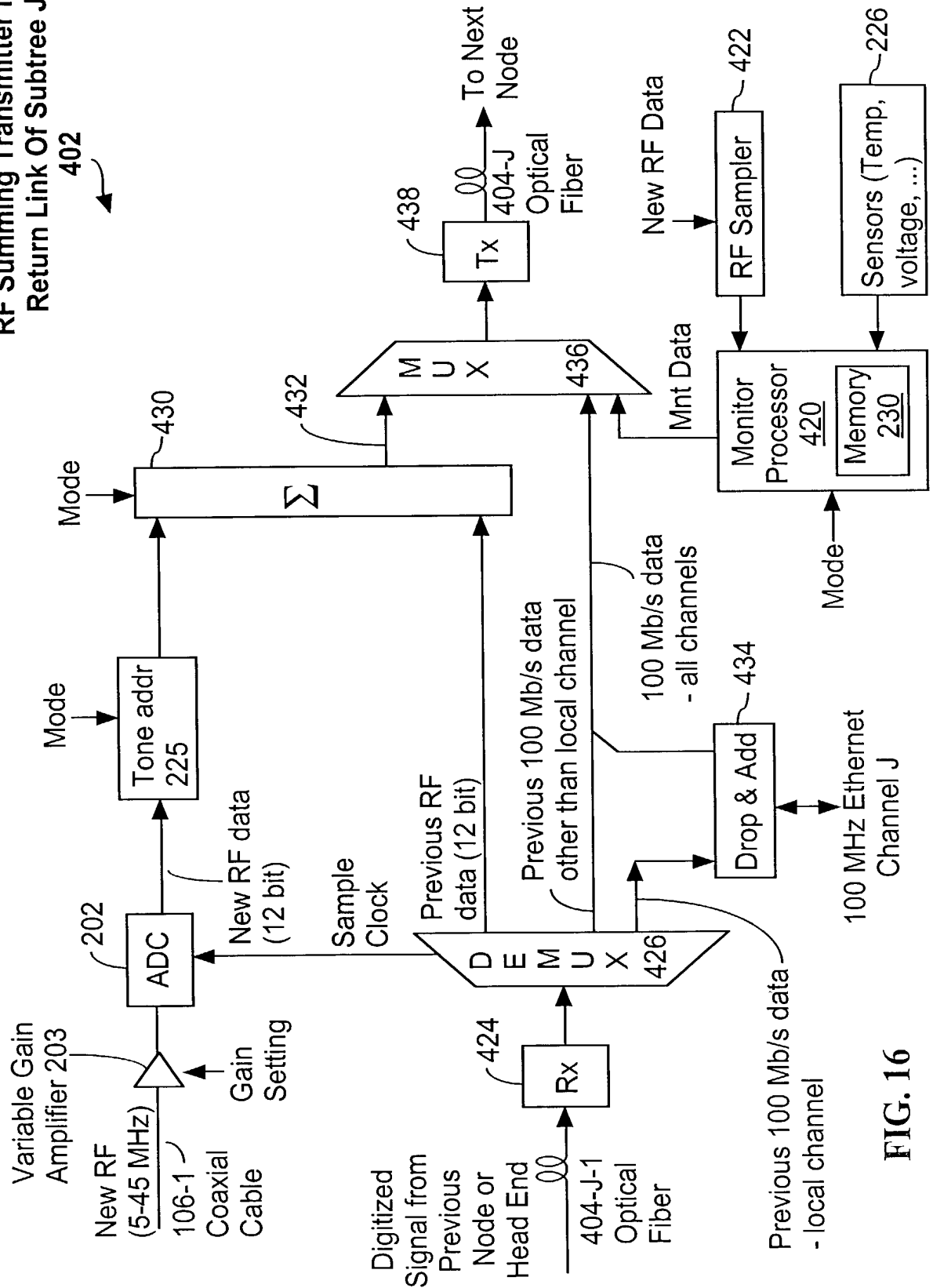


FIG. 15

# RF Summing Transmitter for Return Link Of Subtree J



**FIG. 16**

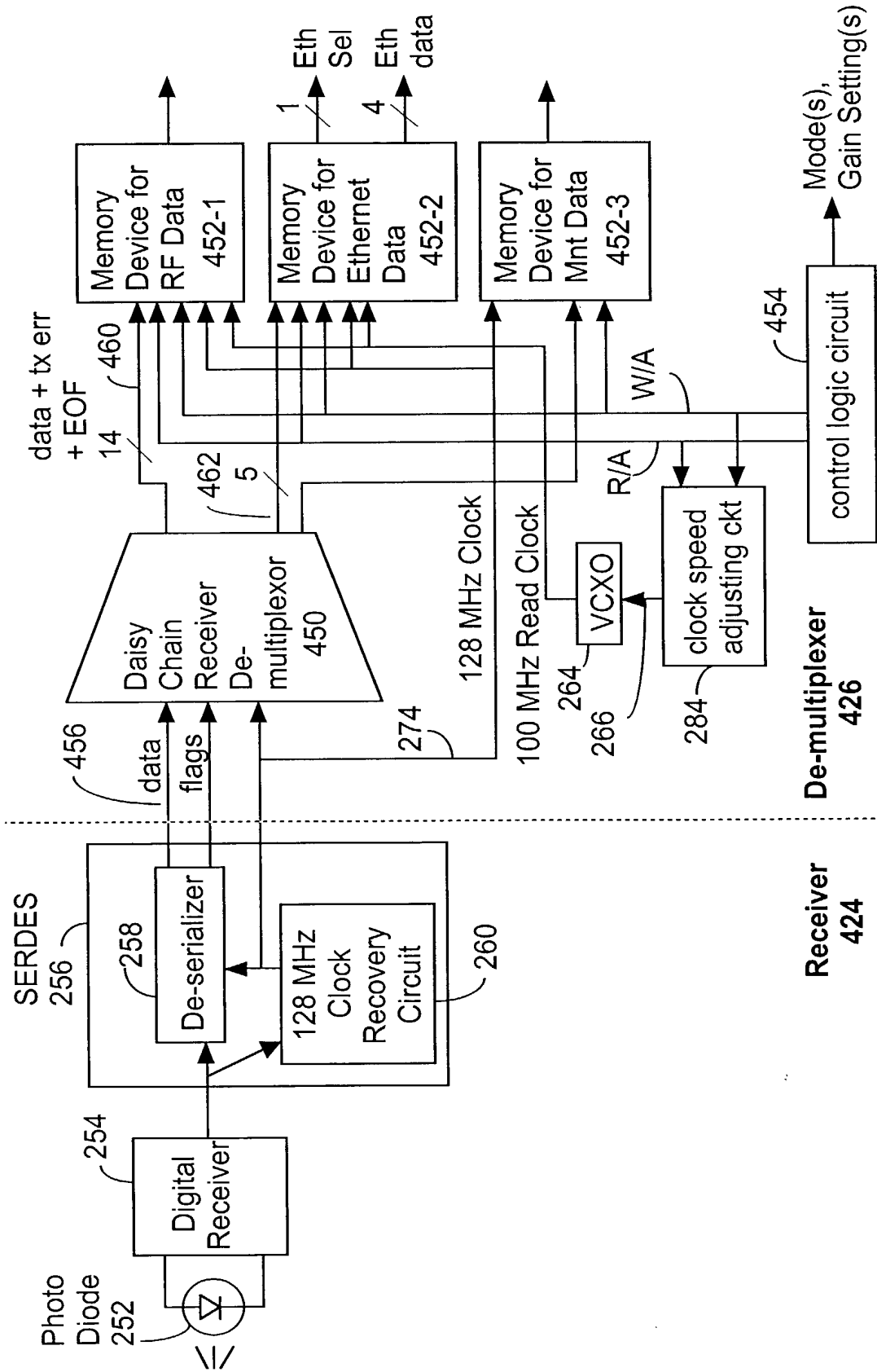


FIG. 17



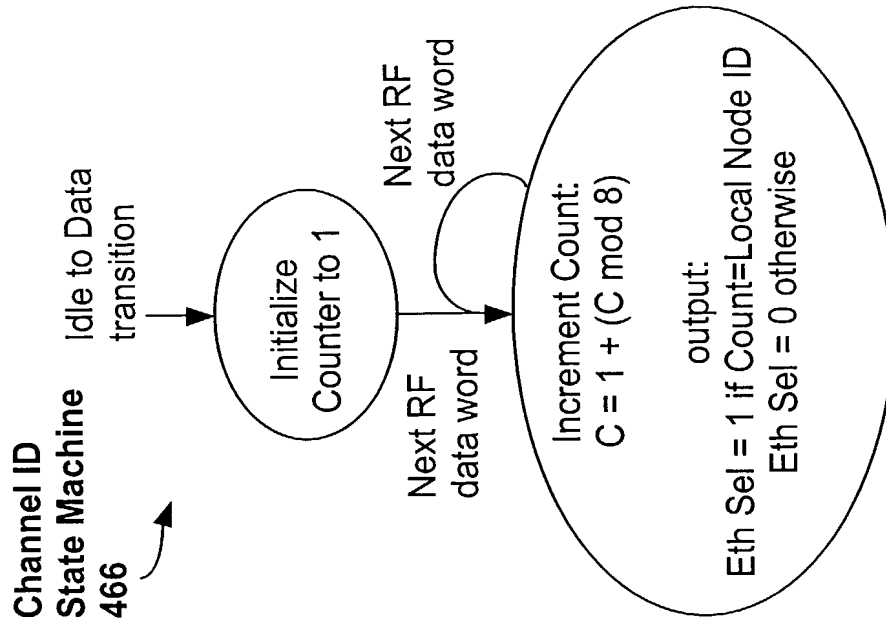


FIG. 19

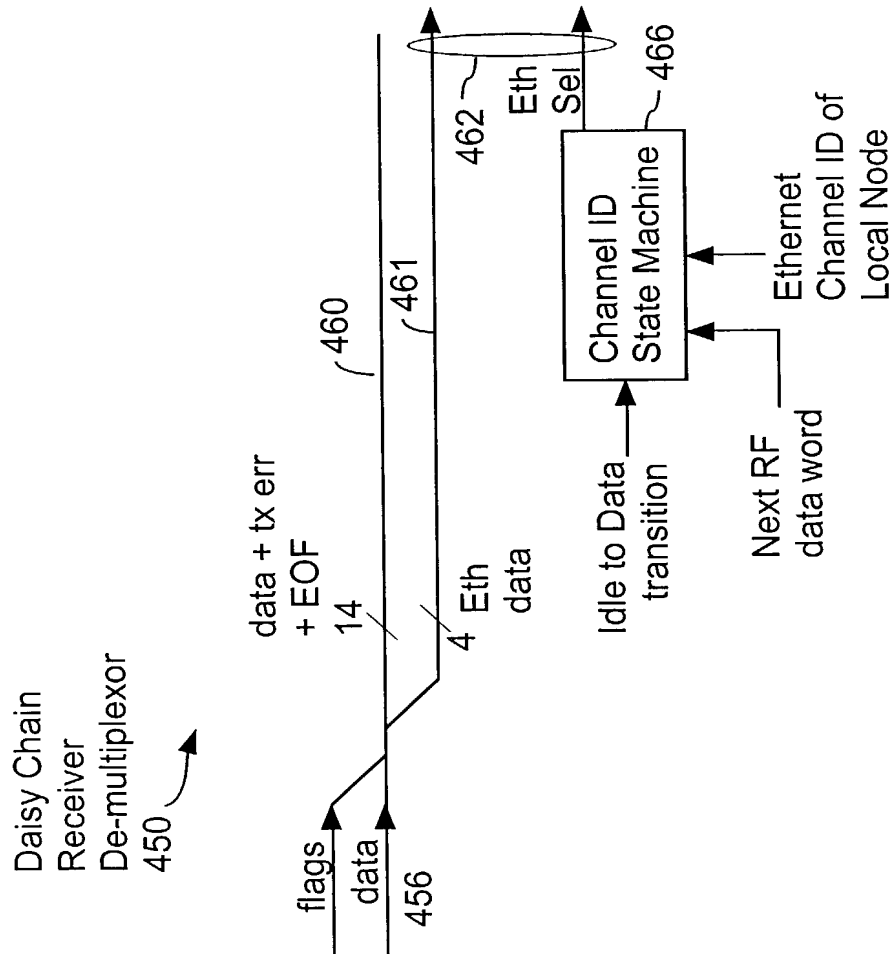


FIG. 18

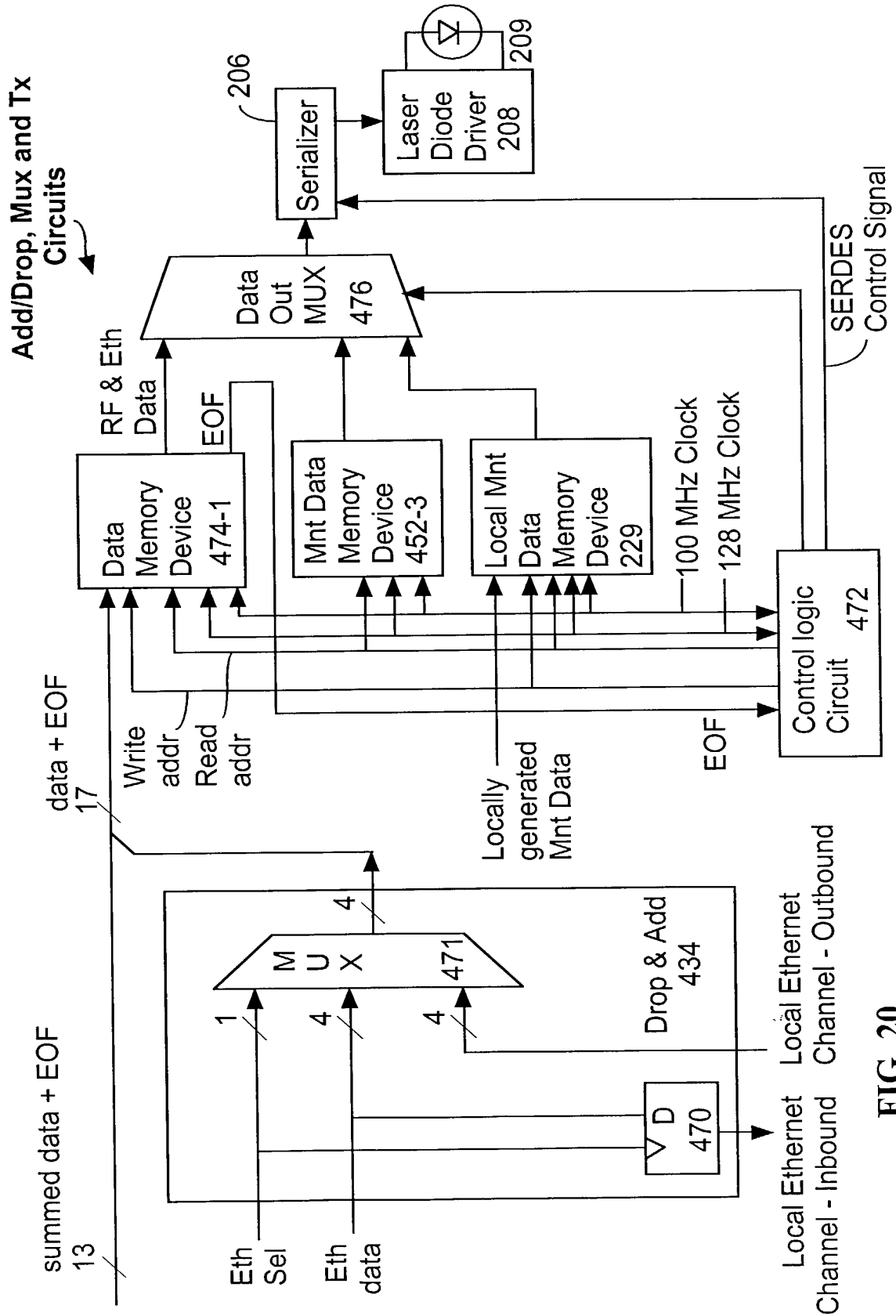


FIG. 20

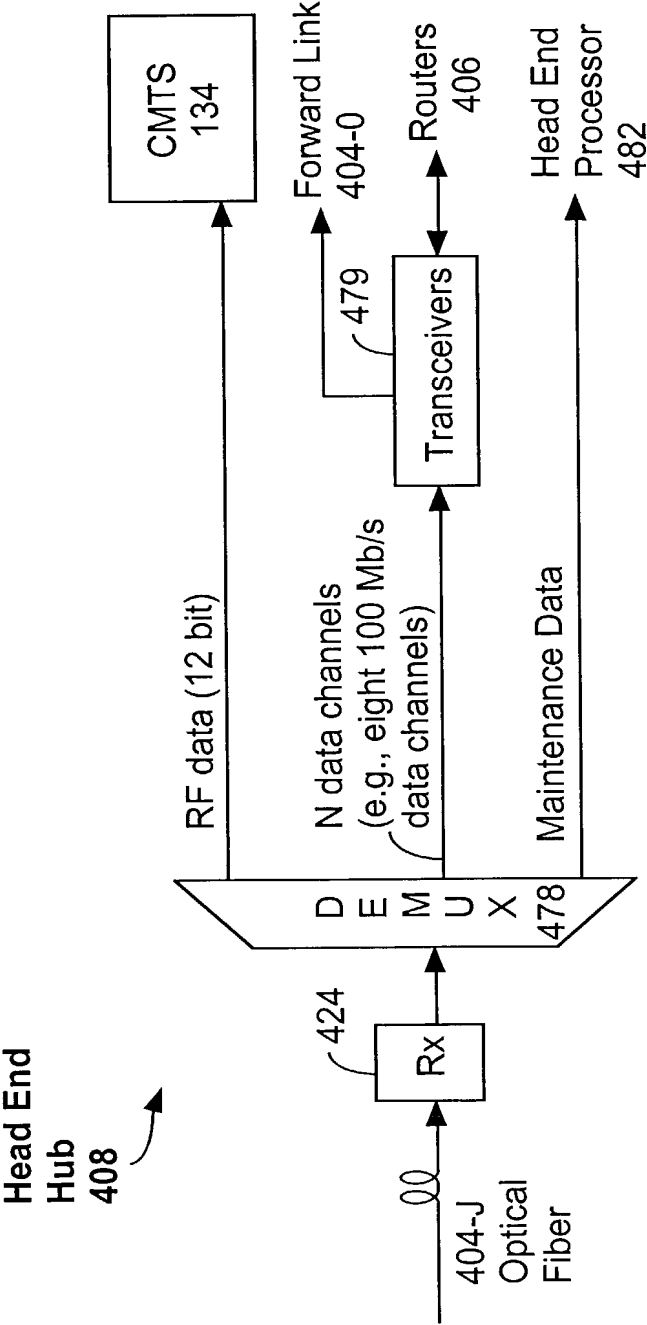


FIG. 21

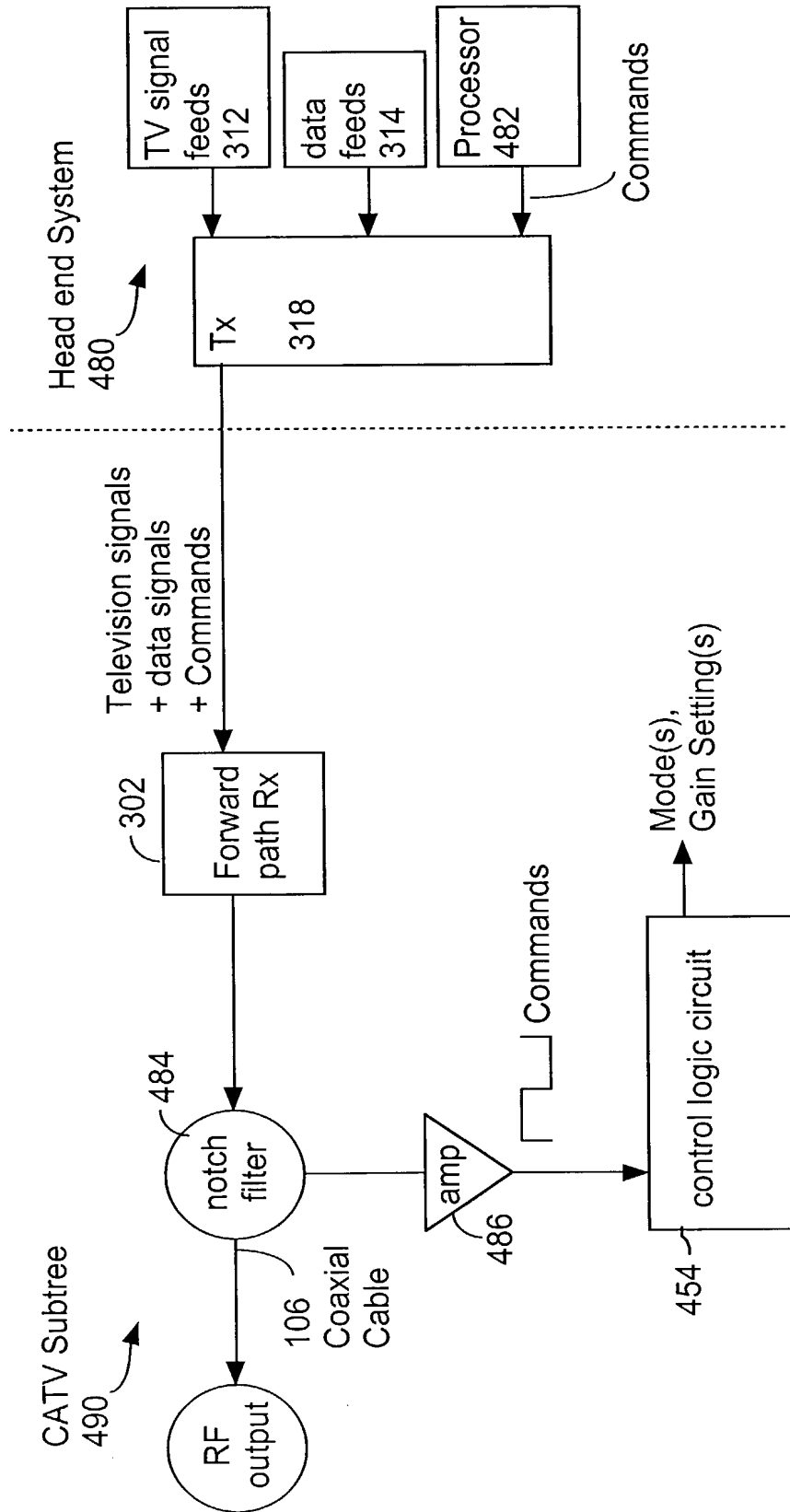


FIG. 22

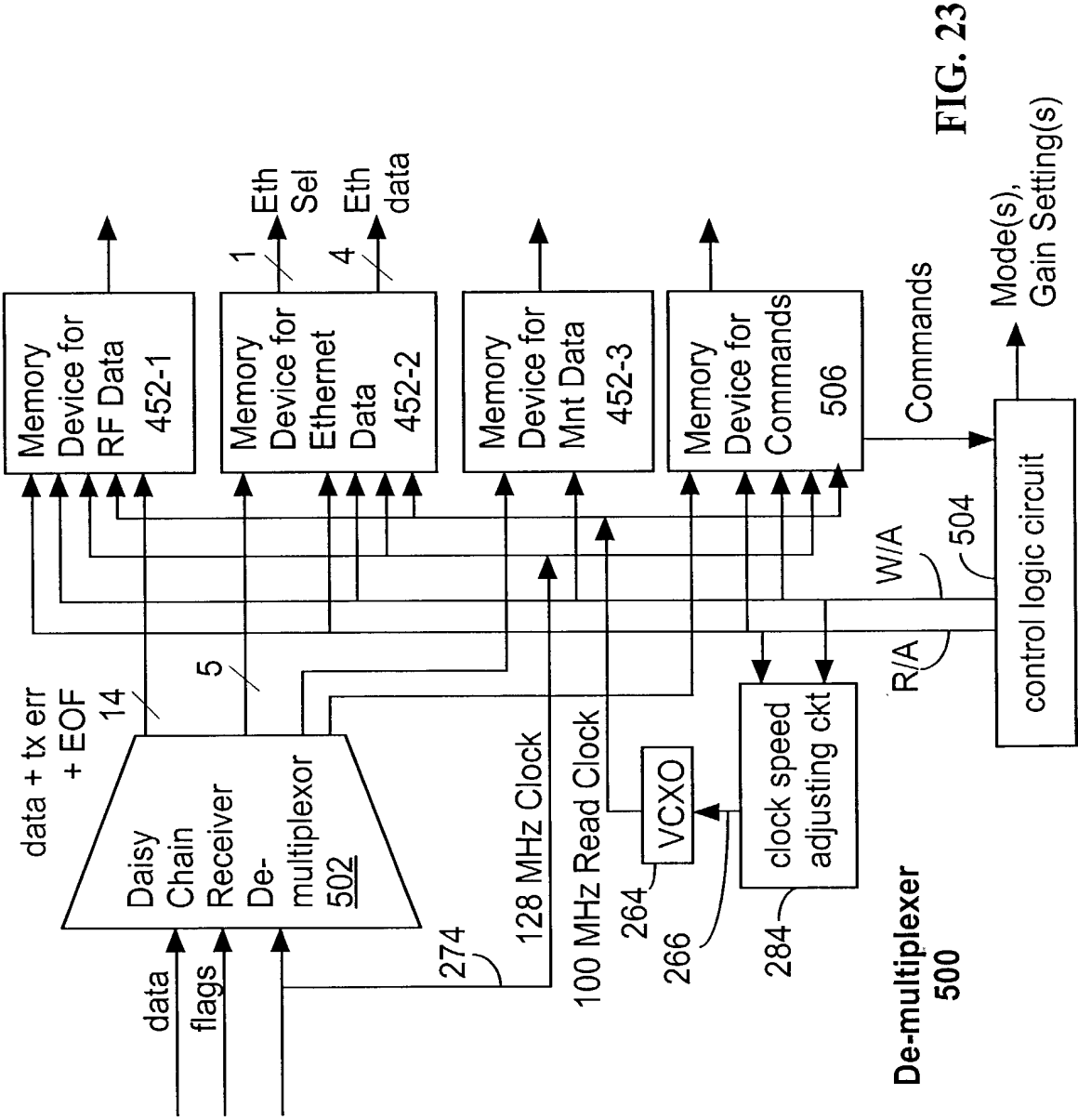


FIG. 23

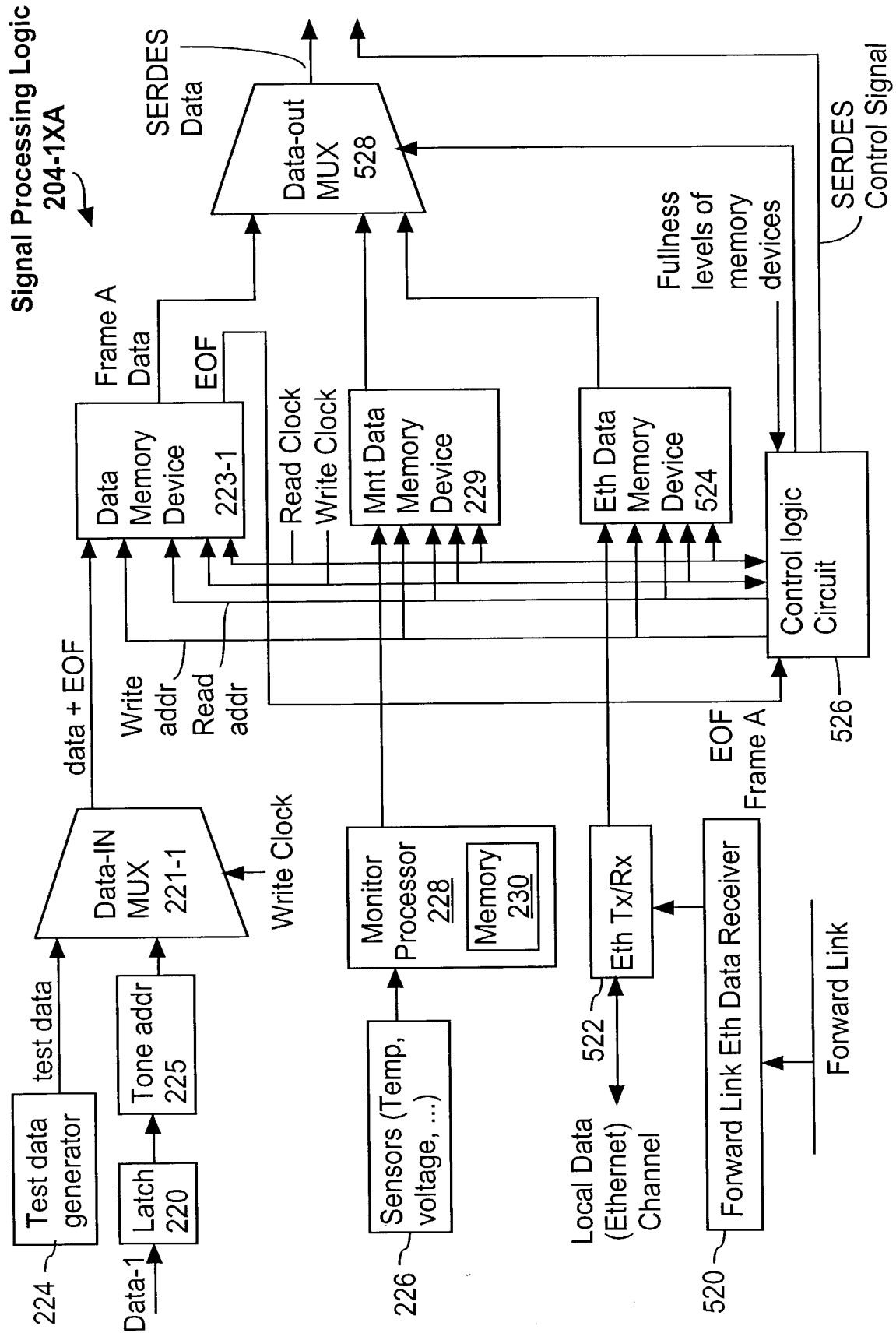


FIG. 24

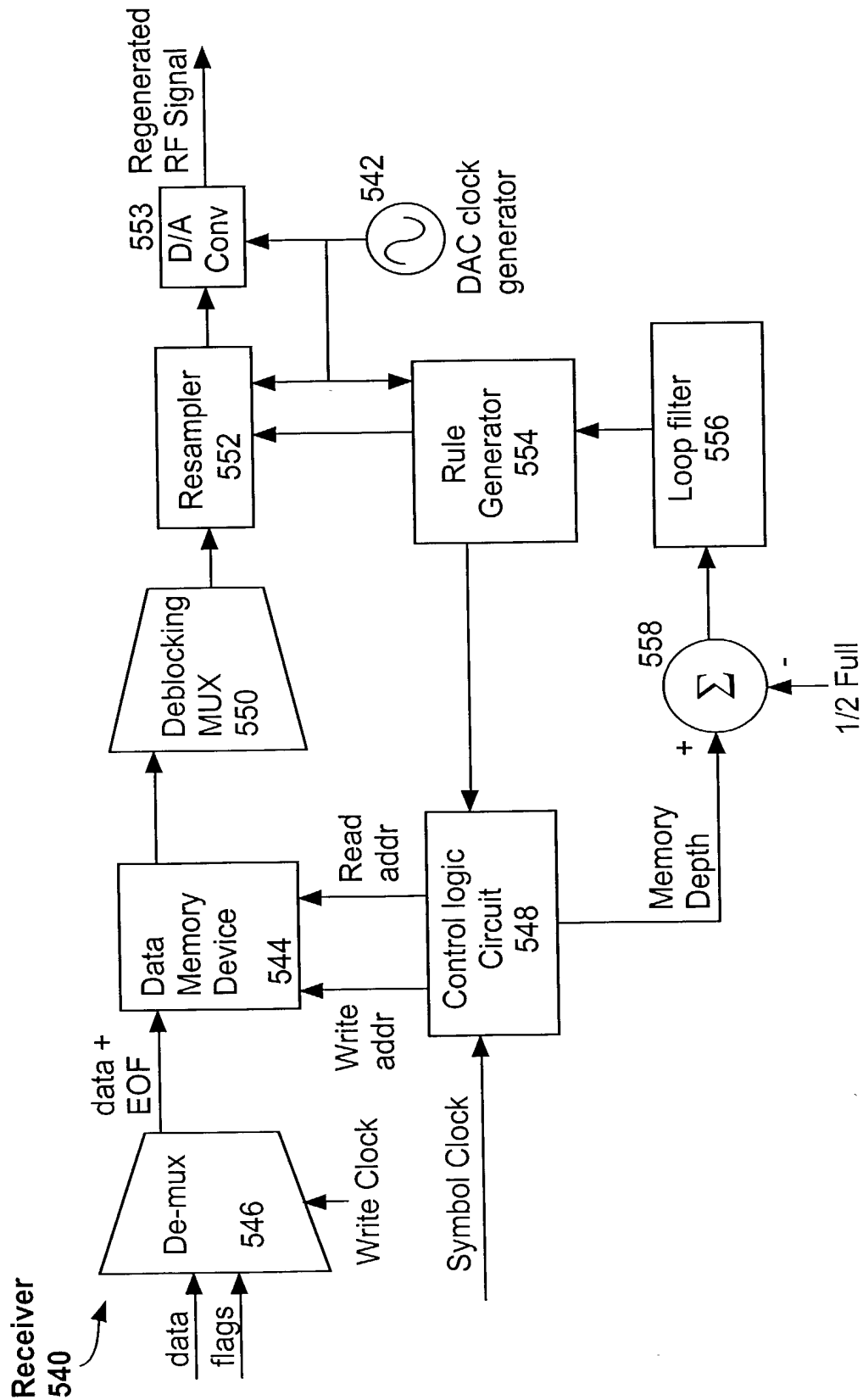


FIG. 25